

UTILITY SERIAL NUMBER	PATENT DATE	PATENT NUMBER
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SERIAL NUMBER 08/965,286	FILING DATE 11/06/97 RULE 60	CLASS 438 251	SUBCLASS 553 565	CRUIED ADT INVT 19-57 2011-2813	EXAMINER NADAN
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APPLICANTS TAKAYUKI GOMI, TOKYO, JAPAN; HIROAKI AMMO, KANAGAWA, JAPAN.

CONTINUING DATA***
VERIFIED THIS APPLN IS A DIV OF 08/762,779 12/10/96, now US. Patent
O.N. 5,976,940

FOREIGN APPLICATIONS***
VERIFIED JAPAN P07-322962 12/12/95
O.N.



Foreign priority claimed 35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no O.N.	AS FILED →	STATE OR COUNTRY JPX	SHEETS DRWGS. 24	TOTAL CLAIMS 10	INDEP. CLAIMS 3	FILING FEE RECEIVED \$790.00	ATTORNEY'S DOCKET NO. P97.2608
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Verified and Acknowledged Examiner's Initials
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ADDRESS

TITLE BIPOLAR TRANSISTOR AND METHOD OF MAKING THE SAME SEMICONDUCTOR DEVICE INCLUDING HIGH SPEED TRANSISTORS AND HIGH VOLTAGE TRANSISTORS DISPOSED ON A SINGLE SUBSTRATE

U.S. DEPT. OF COMM./PAT. & TM-PTO-438L (Rev.12-94)

PARTS OF APPLICATION FILED SEPARATELY		Applications Examiner	
NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Amount Due		Total Claims	Print Claim
Date Paid	Assistant Examiner	Sheets Drawn	Print Fig.
Label Area		Fig. Drawn	Print Fig.
ISSUE FEE		DRAWING	
Amount Due		ISSUE BATCH NUMBER	
Date Paid		PREPARED FOR ISSUE	
Label Area		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code, Title 35, Sections 122, 181, and 366. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

Form PTO-438A
(Rev. 8/92)
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